

Amendments to the Claims:

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (withdrawn) A circuit board assembly which comprises:

an electrically insulating layer;

a conductive printed wiring layer formed on the surface of said electrically insulating layer and including a plurality of conductive paths;

a conductive trace on said electrically insulating layer and means for dissipating a transient;

a surface mount resistor fixed in relation to said trace.

2. (withdrawn) The circuit board assembly as described in claim 1 wherein said surface mount resistor has opposed generally planar lips.

3. (withdrawn) The circuit board assembly as described in claim 1 wherein said surface mount resistor has a generally planar lips and said trace is also generally planar.

4. (withdrawn) The circuit board assembly as described in claim 1 wherein said surface mount resistor has a generally planar lower lip, said trace is also generally planar and said lower lip and said trace are generally parallel.

5. (withdrawn) The circuit board assembly as described in claim 2 wherein said generally planar lips of said surface mount resistor are closer to said trace than the thickness of said surface mount resistor.
6. (withdrawn) The circuit board assembly as described in claim 2 wherein said lips and said trace are parallel.
7. (withdrawn) The circuit board assembly as described in claim 4 wherein a single geometric plane extends through substantially all of said lips and all of said trace.
8. (withdrawn) A circuit board assembly as described in claim 7 wherein the lower surface of said lips and the lower surface of said trace are substantially coplanar.
9. (withdrawn) A circuit board assembly as described in claim 7 wherein the upper surface of said lower lip and the upper surface of said trace are substantially coplanar.
10. (withdrawn) A circuit board assembly as described in claim 7 wherein the lower surface of said lower lip and the lower surface of said trace are substantially coplanar and in addition the upper surface of said lip and the upper surface of said trace are substantially coplanar.
11. (withdrawn) A circuit board assembly as described in claim 10 wherein said surface mount resistor has a height of t and the spacing between said lip and said trace is less than t .
12. (withdrawn) A circuit board assembly as described in claim 11 wherein the spacing between said lip and said trace is no more than one half t .

Claims 13-26 (cancelled)

27. (withdrawn) A circuit board assembly which comprises:

an electrically insulating layer;

a conductive printed wiring layer formed on the surface of said electrically insulating layer and including a plurality of conductive paths;

a conductive trace on said electrically insulating layer and means for dissipating a transient;

a first surface mount resistor fixed in closely spaced relation to said trace;

a second surface mount resistor fixed in closely space relation to said trace.

28. (withdrawn) The circuit board assembly as described in claim 27 wherein each of said surface mount resistors has opposed generally planar lips.

29. (withdrawn) The circuit board assembly as described in claim 27 wherein each of said surface mount resistors has a generally planar lips and said trace is also generally planar.

30. (withdrawn) The circuit board assembly as described in claim 27 wherein each of said surface mount resistors has a generally planar lower lip, said trace is also generally planar and all of said lower lips and said trace are generally parallel.

31. (withdrawn) The circuit board assembly as described in claim 27 wherein said generally planar lips of said surface mount resistors are closer to said trace than the thickness of said surface mount resistors.
32. (withdrawn) The circuit board assembly as described in claim 28 wherein said lips and said trace are parallel.
33. (withdrawn) The circuit board assembly as described in claim 32 wherein a single geometric plane extends through substantially all of said lips and all of said trace.
34. (withdrawn) A circuit board assembly as described in claim 33 wherein the lower surface of said lips and the lower surface of said trace are substantially coplanar.
35. (withdrawn) A circuit board assembly as described in claim 33 wherein the upper surface of said lower lips and the upper surface of said trace are substantially coplanar.
36. (withdrawn) A circuit board assembly as described in claim 33 wherein the lower surface of said lower lips and the lower surface of said trace are substantially coplanar and in addition the upper surface of said lips and the upper surface of said trace are substantially coplanar.
37. (withdrawn) A circuit board assembly as described in claim 33 wherein said surface mount resistor has a height of t and the spacing between each of said lips and said trace is less than t .
38. (withdrawn) A circuit board assembly as described in claim 37 wherein the spacing between each of said lips and said trace is no more than one half t .

39. (withdrawn) A circuit board assembly as described in claim 27 wherein the distance between said first and second resistors is greater than the height of each resistor.

40. (withdrawn) A circuit board assembly as described in claim 28 wherein the distance between said first and second resistors is greater than the height of each resistor.

41. (withdrawn) A circuit board assembly as described in claim 29 wherein the distance between said first and second resistors is greater than the height of each resistor.

42. (withdrawn) A circuit board assembly as described in claim 30 wherein the distance between said first and second resistors is greater than the height of each resistor.

43. (withdrawn) A circuit board assembly as described in claim 31 wherein the distance between said first and second resistors is greater than the height of each resistor.

44. (withdrawn) A circuit board assembly as described in claim 32 wherein the distance between said first and second resistors is greater than the height of each resistor.

45. (withdrawn) A circuit board assembly as described in claim 33 wherein the distance between said first and second resistors is greater than the height of each resistor.

46. (withdrawn) A circuit board assembly as described in claim 34 wherein the distance between said first and second resistors is greater than the height of each resistor.

47. (withdrawn) A circuit board assembly as described in claim 35 wherein the distance between said first and second resistors is greater than the height of each resistor.

48. (withdrawn) A circuit board assembly as described in claim 36 wherein the distance between said first and second resistors is greater than the height of each resistor.

49. (withdrawn) A circuit board assembly as described in claim 38 wherein the distance between said first and second resistors is greater than the height of each resistor.

50. (withdrawn) A circuit board assembly as described in claim 27 wherein the distance between said first and second resistors is at least three times the height of each resistor.

51. (currently amended) A circuit board assembly as described in claim 24 28 wherein the distance between said first and second resistors is at least three times the height of each resistor.

52. (withdrawn) A circuit board assembly as described in claim 29 wherein the distance between said first and second resistors is at least three times the height of each resistor.

53. (withdrawn) A circuit board assembly as described in claim 30 wherein the distance between said first and second resistors is at least three times the height of each resistor.

54. (withdrawn) A circuit board assembly as described in claim 32 one wherein the distance between said first and second resistors is at least three times the height of each resistor.

55. (withdrawn) A circuit board assembly as described in claim 32 wherein the distance between said first and second resistors is at least three times the height of each resistor.

56. (withdrawn) A circuit board assembly as described in claim 33 wherein the distance between said first and second resistors is at least three times the height of each resistor.

57. (withdrawn) A circuit board assembly as described in claim 34 wherein the distance between said first and second resistors is at least three times the height of each resistor.

58. (withdrawn) A circuit board assembly as described in claim 35 wherein the distance between said first and second resistors is at least three times the height of each resistor.

60. (withdrawn) A circuit board assembly as described in claim 37 wherein the distance between said first and second resistors is at least three times the height of each resistor.

60. (withdrawn) A circuit board assembly as described in claim 38 wherein the distance between said first and second resistors is at least three times the height of each resistor.

61. (withdrawn) A circuit board assembly as described in claim 39 wherein the distance between said first and second resistors is at least three times the height of each resistor.

62. (Previously presented) A circuit protection system for dissipating transients without the use of transorbs or metal oxide varistors which comprises:

a printed circuit board;

a surface mount component mounted on said printed circuit board, said surface component having first and second end caps, said first and second end caps each having a first edge;

a conductive trace on said printed circuit board having first and second opposed edges extending intermediate said first and second end caps, said first and second opposed edges being coplanar and thereby defining a plane, said plane intersecting said first edge of said first end cap and intersecting said first edge of said second end cap, said first edge of said trace being disposed in parallel spaced relation to said first edge of said first end cap and said second edge of said trace being disposed in parallel spaced relation to said first edge of said second end cap.

63. (currently amended) The circuit protection system as described in claim 62 wherein the dimension of the space intermediate

- (1) said first edge of said trace and said first edge of said first end cap and
- (2) said second edge of said trace and said first edge of said second end cap

are both substantially equal to X.

64. (Previously presented) The circuit protection system as described in claim 63 wherein said surface mount component has a height dimension t and X is less than t.

65. (Previously presented) The circuit protection system as described in claim 63 wherein X is approximately .01 inch.

66. (Previously presented) A circuit protection system for dissipating transients without the use of transorbs or metal oxide varistors which comprises:

a printed circuit board;

a first surface mount component mounted on said printed circuit board, said surface component having first and second end caps, said first and second end caps each having a first edge;

a second surface mount component mounted on said printed circuit board, said surface component having first and second end caps, said first and second end caps each having a first edge;

a conductive trace on said printed circuit board having first and second opposed edges extending intermediate said first and second end caps of said first surface mounted component, said first and second opposed edges being coplanar and thereby defining a plane, said plane intersecting said first edge of said first end cap of said first surface mounted component and intersecting said first edge of said second end cap of said first surface mounted component, said first edge of said trace being disposed in parallel spaced relation to said first edge of said first end cap of said first surface mounted component and said second edge of said trace is disposed in parallel spaced relation to said first edge of said second end cap of said first surface mounted component; and

said conductive trace on said printed circuit board having said first and second opposed edges extending intermediate said first and second end caps of said second surface mounted component, said plane intersecting said first edge of said first end cap of said second surface mounted component and intersecting said first edge of said second end cap of said second surface mounted component, said first edge of said trace being disposed in parallel spaced relation to said first edge of said first end cap of said second surface mounted

component and said second edge of said trace being disposed in parallel spaced relation to said first edge of said second end cap of said second surface mounted component.

67. (Previously presented) The circuit protection system as described in claim 66 wherein dimension of the space intermediate (1) said first edge of said trace and said first edge of said first end cap of said first surface mounted component and (2) said second edge of said trace and said first edge of said second end cap of said first surface mounted component, (3) said first edge of said trace and said first edge of said first end cap of said second surface mounted component and (4) said second edge of said trace and said first edge of said second end cap of said second surface mounted component are all substantially equal to X.

68. (Previously presented) The circuit protection system as described in claim 65 wherein said surface mount component has a height dimension t and X is less than t.

69. (Previously presented) The circuit protection system as described in claim 67 wherein X is approximately .01 inch.

70. (Previously presented) The circuit protection system as described in claim 67 wherein the minimum spacing between the respective end caps of said first and second surface mounted components is at least three times the dimension X.

71. (Previously presented) A circuit protection system for dissipating transients without the use of transorbs or metal oxide varistors which comprises:

a printed circuit board;

a first surface mount component mounted on said printed circuit board, said surface component having first and second end caps, said first and second end caps each having a first edge;

a second surface mount component mounted on said printed circuit board, said surface component having first and second end caps, said first and second end caps each having a first edge;

a first conductive trace on said printed circuit board having first and second opposed edges extending intermediate said first and second end caps of said first surface mounted component, said first and second opposed edges being coplanar and thereby defining a plane, said plane intersecting said first edge of said first end cap of said first surface mounted component and intersecting said first edge of said second end cap of said first surface mounted component, said first edge of said first trace being disposed in parallel spaced relation to said first edge of said first end cap of said first surface mounted component and said second edge of said first trace being disposed in parallel spaced relation to said first edge of said second end cap of said first surface mounted component;

said first conductive trace on said printed circuit board having said first and second opposed edges extending intermediate said first and second end caps of said second surface mounted component, said plane intersecting said first edge of said first end cap of said second surface mounted component and intersecting said first edge of said second end cap of said second surface mounted component, said first edge of said first trace being disposed in parallel spaced relation to said first edge of said first end cap of said second surface mounted component and said second edge of said first trace being disposed in parallel spaced relation to said first edge of said second end cap of said second surface mounted component;

a third surface mount component mounted on said printed circuit board, said third surface component having first and second end caps, said first and second end caps each having a first edge;

a second conductive trace on said printed circuit board having first and second opposed edges extending intermediate said first and second end caps of said third surface mount component, said first and second opposed edges of said second conductive trace being coplanar and thereby defining a plane, said plane intersecting said first edge of said first end cap of said third surface mount component and intersecting said first edge of said second end cap of said third surface mount component, said first edge of said second trace being disposed in parallel spaced relation to said first edge of said first end cap of said third surface mount component and said second edge of said second trace being disposed in parallel spaced relation to said first edge of said second end cap of said third surface mount component.

72. (Previously presented) The circuit protection system as described in claim 71 wherein dimension of the space intermediate (1) said first edge of said first trace and said first edge of said first end cap of said first surface mounted component, (2) said second edge of said first trace and said first edge of said second end cap of said first surface mounted component, (3) said first edge of said first trace and said first edge of said first end cap of said second surface mounted component and (4) said second edge of said first trace and said first edge of said second end cap of said second surface mounted component are all equal to X, (5) said first edge of said second trace and said first edge of said first end cap of said third surface mounted component, and (6) said second edge of said second trace and said first edge of said second end cap of said third surface mounted component are all substantially equal to X.

73. (Previously presented) The circuit protection system as described in claim 72 wherein each surface mount component has a height dimension t and X is less than t.

74. (Previously presented) The circuit protection system as described in claim 72 wherein X is approximately .01 inch.

75. (Previously presented) The circuit protection system as described in claim 71 wherein the minimum spacing between the respective end caps of said first and second surface mounted components is at least three times the dimension X.

76. (Previously presented) A circuit protection system for dissipating transients without the use of transorbs or metal oxide varistors which comprises:

a printed circuit board;

a surface mount resistor mounted on said printed circuit board, said surface resistor having first and second end caps, said first and second end caps each having a first edge;

a conductive trace on said printed circuit board having first and second opposed edges extending intermediate said first and second end caps, said first and second opposed edges being coplanar and thereby defining a plane, said plane intersecting said first edge of said first end cap and intersecting said first edge of said second end cap, said first edge of said trace being disposed in parallel spaced relation to said first edge of said first end cap and said second edge of said trace being disposed in parallel spaced relation to said first edge of said second end cap.

77. (Previously presented) The circuit protection system as described in claim 76 wherein dimension of the space intermediate (1) said first edge of said trace and

said first edge of said first end cap and (2) said second edge of said trace and said first edge of said second end cap are both substantially equal to X.

78. (Previously presented) The circuit protection system as described in claim 77 wherein said surface mount resistor has a height dimension t and X is less than t.

79. (Previously presented) The circuit protection system as described in claim 78 wherein X is approximately .01 inch.

80. (Previously presented) A circuit protection system for dissipating transients without the use of transorbs or metal oxide varistors which comprises:

a printed circuit board;

a first surface mount resistor mounted on said printed circuit board, said surface resistor having first and second end caps, said first and second end caps each having a first edge;

a second surface mount resistor mounted on said printed circuit board, said surface resistor having first and second end caps, said first and second end caps each having a first edge;

a conductive trace on said printed circuit board having first and second opposed edges extending intermediate said first and second end caps of said first surface mounted resistor, said first and second opposed edges being coplanar and thereby defining a plane, said plane intersecting said first edge of said first end cap of said first surface mounted resistor and intersecting said first edge of said second end cap of said first surface mounted resistor, said first edge of said trace being disposed in parallel spaced relation to said first edge of said first end cap of said first surface mounted resistor and said second edge of said trace is

disposed in parallel spaced relation to said first edge of said second end cap of said first surface mounted resistor; and

said conductive trace on said printed circuit board having said first and second opposed edges extending intermediate said first and second end caps of said second surface mounted resistor, said plane intersecting said first edge of said first end cap of said second surface mounted resistor and intersecting said first edge of said second end cap of said second surface mounted resistor, said first edge of said trace being disposed in parallel spaced relation to said first edge of said first end cap of said second surface mounted resistor and said second edge of said trace being disposed in parallel spaced relation to said first edge of said second end cap of said second surface mounted resistor.

81. (Previously presented) The circuit protection system as described in claim 80 wherein dimension of the space intermediate (1) said first edge of said trace and said first edge of said first end cap of said first surface mounted resistor and (2) said second edge of said trace and said first edge of said second end cap of said first surface mounted resistor, (3) said first edge of said trace and said first edge of said first end cap of said second surface mounted resistor and (4) said second edge of said trace and said first edge of said second end cap of said second surface mounted resistor are all substantially equal to X.

82. (Previously presented) The circuit protection system as described in claim 81 wherein said surface mount resistor has a height dimension t and X is less than t.

83. (Previously presented) The circuit protection system as described in claim 82 wherein X is approximately .01 inch.

84. (Previously presented) The circuit protection system as described in claim 81 wherein the minimum spacing between the respective end caps of said first and second surface mounted resistors is at least three times the dimension X.

85. (Previously presented) A circuit protection system for dissipating transients without the use of transorbs or metal oxide varistors which comprises:

a printed circuit board;

a first surface mount resistor mounted on said printed circuit board, said surface resistor having first and second end caps, said first and second end caps each having a first edge;

a second surface mount resistor mounted on said printed circuit board, said surface resistor having first and second end caps, said first and second end caps each having a first edge;

a first conductive trace on said printed circuit board having first and second opposed edges extending intermediate said first and second end caps of said first surface mounted resistor, said first and second opposed edges being coplanar and thereby defining a plane, said plane intersecting said first edge of said first end cap of said first surface mounted resistor and intersecting said first edge of said second end cap of said first surface mounted resistor, said first edge of said first trace being disposed in parallel spaced relation to said first edge of said first end cap of said first surface mounted resistor and said second edge of said first trace being disposed in parallel spaced relation to said first edge of said second end cap of said first surface mounted resistor;

said first conductive trace on said printed circuit board having said first and second opposed edges extending intermediate said first and second end caps of said second surface mounted resistor, said plane intersecting said first edge of said first end cap of said second surface mounted resistor and intersecting said first edge of said second end cap of said second surface mounted resistor, said first edge of said first trace being disposed in parallel spaced relation to said first

edge of said first end cap of said second surface mounted resistor and said second edge of said first trace being disposed in parallel spaced relation to said first edge of said second end cap of said second surface mounted resistor;

a third surface mount resistor mounted on said printed circuit board, said third surface resistor having first and second end caps, said first and second end caps each having a first edge;

a second conductive trace on said printed circuit board having first and second opposed edges extending intermediate said first and second end caps of said third surface mount resistor, said first and second opposed edges of said second conductive trace being coplanar and thereby defining a plane, said plane intersecting said first edge of said first end cap of said third surface mount resistor and intersecting said first edge of said second end cap of said third surface mount resistor, said first edge of said second trace being disposed in parallel spaced relation to said first edge of said first end cap of said third surface mount resistor and said second edge of said second trace being disposed in parallel spaced relation to said first edge of said second end cap of said third surface mount resistor.

86. (Previously presented) The circuit protection system as described in claim 85 wherein dimension of the space intermediate (1) said first edge of said first trace and said first edge of said first end cap of said first surface mounted resistor, (2) said second edge of said first trace and said first edge of said second end cap of said first surface mounted resistor, (3) said first edge of said first trace and said first edge of said first end cap of said second surface mounted resistor and (4) said second edge of said first trace and said first edge of said second end cap of said second surface mounted resistor are all equal to X, (5) said first edge of said second trace and said first edge of said first end cap of said third surface mounted resistor, and (6) said second edge of said second trace and said first

edge of said second end cap of said third surface mounted resistor are all substantially equal to X.

87. (Previously presented) The circuit protection system as described in claim 86 wherein each surface mount resistor has a height dimension t and X is less than t.

88. (Previously presented) The circuit protection system as described in claim 86 wherein X is approximately .01 inch.

89. (Previously presented) The circuit protection system as described in claim 71 wherein the minimum spacing between the respective end caps of said first and second surface mounted resistors is at least three times the dimension X.